

## Specifications

**Programmable ROM chips:** Various EP/EEPROM chips (described in the socket adapter section)

**Buffer memory capacity:** 2 Mbytes (16 Mbits)

**Program power supply:**

$V_{CC}$  power supply: +4.75 V  $\pm$  0.25 V, 300 mA (max.)

+5 V  $\pm$  0.25 V, 300 mA (max.)

+5.25 V  $\pm$  0.25 V, 300 mA (max.)

+6.00 V  $\pm$  0.25 V, 300 mA (max.)

+6.25 V  $\pm$  0.25 V, 300 mA (max.)

+6.5 V  $\pm$  0.25 V, 300 mA (max.)

$V_{PP}$  power supply: +21.0 V  $\pm$  0.50 V, 100 mA (max.)

+13.0 V  $\pm$  0.30 V, 200 mA (max.)

+12.75 V  $\pm$  0.30 V, 200 mA (max.)

+12.5 V  $\pm$  0.30 V, 200 mA (max.)

+5.0 V  $\pm$  0.25 V, 50 mA (max.)

**Output voltage comparison level:**

$V_{OL}$ : +0.50 V  $\pm$  50 mV ( $I_{OL}$  1.8 mA  $\pm$  0.2 mA)

$V_{OH}$ : +2.35 V  $\pm$  100 mV

**Standard interface:**

Serial I/O interface: RS-232C

Parallel I/O interface: Centronics

**Device functions:**

Blank check, Programming, Read check, BPR continuous operation, PR continuous operation, Copy/read check, Erase/blank check (EEPROM only), Option, Security

**Address mode:** Normal mode and page mode

**Data mode:**

8-bit wide ROM, Normal, 16-bit split

32-bit split (2 split simultaneous write possible)

16-bit wide ROM, normal

32-bit split (data exchange possible)

**Program method:**

Intel method, Intel quick method, Fujitsu method and other high-speed programming methods

**EPROM protection function:** Checks for power-down when a device is inserted, opposite direction insertion and erroneous insertion (ON/OFF possible)

**Reliability check functions:**

$V_{CC}$  margin check (2 points),  $V_{OL}/V_{OH}$  level check, Data sum check

**Self-diagnostic functions:** Internal memory check, System memory check

**Manual diagnostic functions:**

MUP address check, MUP data check, Program voltage check,

Program timing check, Serial I/O check

**Alarm functions:** Key switch key tone (ON/OFF possible), Pass/fail alarm tone (ON/OFF possible)

**Data edit functions:** Insert, Delete, Compliment, Block store, Block move, Block search, Block change, RAM clear

**Automatic setting functions:** ROM type, I/O conditions, Translation format, Various settings (Precheck, Last address stop, Time-out, ID, Alarm ON/OFF), Backedup by EEPROM

**Translation format:**

DG binary, DEC binary, ASCII HEX, INTELLEC HEX, MOTOROLA S, EXTENDED TEKHEX, ASM-86 HEXADECIMAL, HP64000ABS, TEXTRONIX•HEXADECIMAL

**General Specifications**

**Display:** 16 characters  $\times$  2 lines

**Power requirement:** 90 VAC to 250 VAC

**Power frequency:** 48 to 66 Hz

**Environment:** Temperature 0°C to +40°C, Humidity 85% or less

**Storage temperature:** -15°C to +60°C

**Power consumption:** 37 VA or less

**Dimensions:** Approx. 280(W)  $\times$  59(H)  $\times$  210(D) mm (excluding socket adapter)

Approx. 280(W)  $\times$  78(H)  $\times$  210(D) mm (with R49451A mounted)

**Mass:** 1.5 kg or less (excluding socket adapter)

1.7 kg or less (with R49451A mounted)

**Socket Adapter/standard accessory:**

**R49451A** (standard): 32 pin  $\times$  2, 40 pin  $\times$  1, DIP type

**Accessories (Optional)**

**A01242-200** Connection cable for RS-232C (for PC9801)

**A01224** Connection cable for Centronics

**Options****Major socket adapters for R4945A (sold separately)**

- **R4951A** (standard): 32-pin  $\times$  2, 40-pin  $\times$  1, DIP-type
- **R49446A/B/C:** For PLCC
- **R49451C:** Mask pin array, 40-pin, 42-pin, DIP-type
- **R49451D:** Flash ROM
- **R49442C:** 40-pin for one-chip, 8748/8749 series